

FIG. 1 (Prior Art)

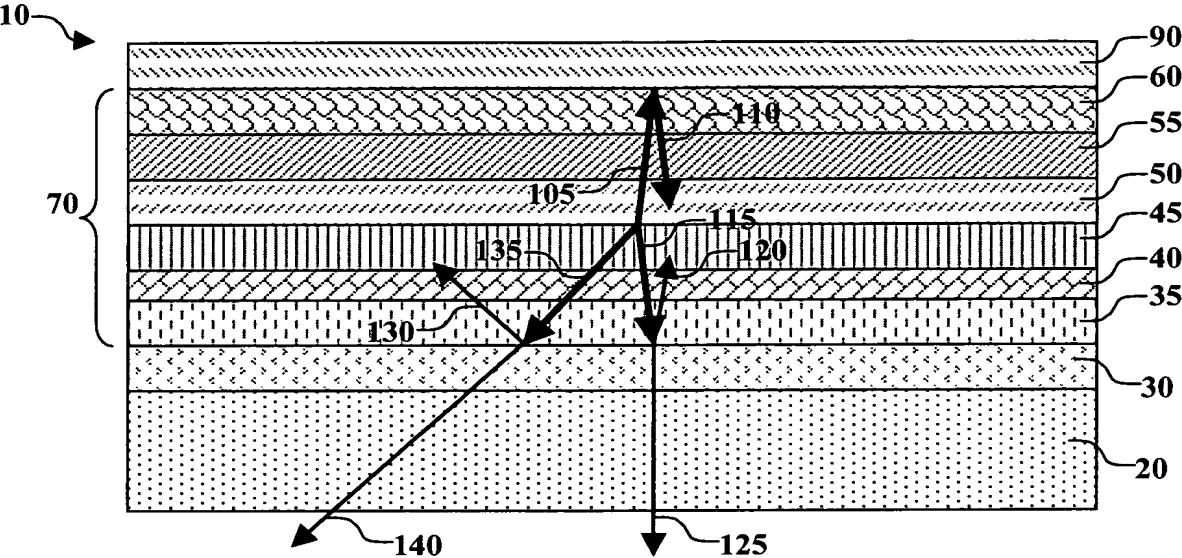


FIG. 2 (Prior Art)

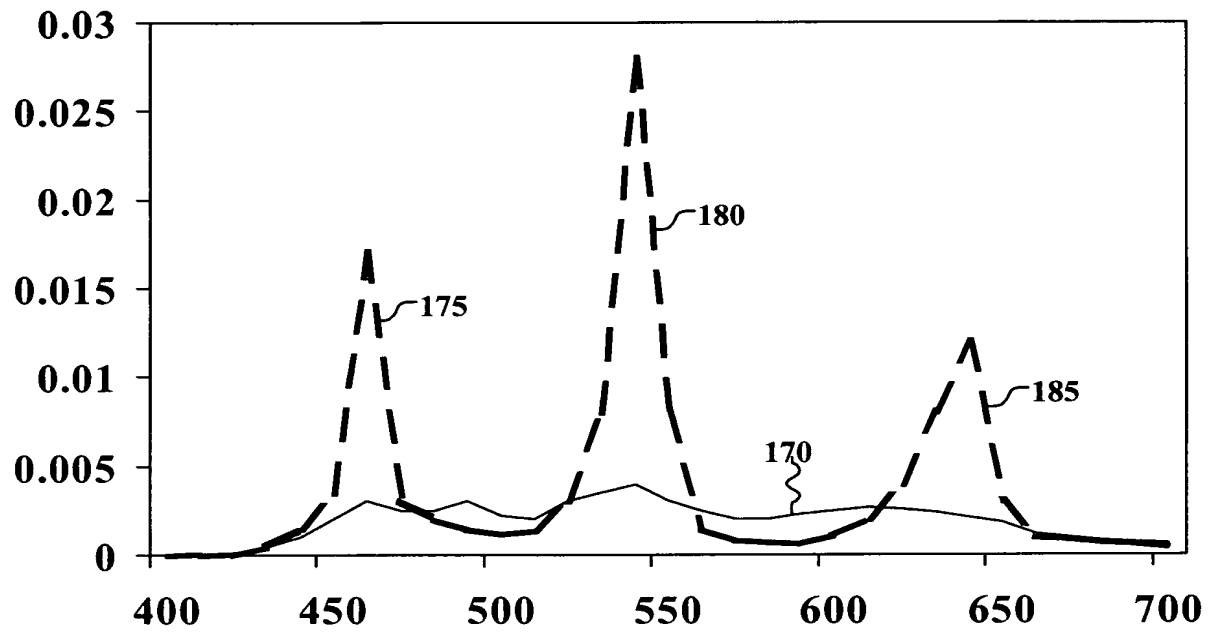
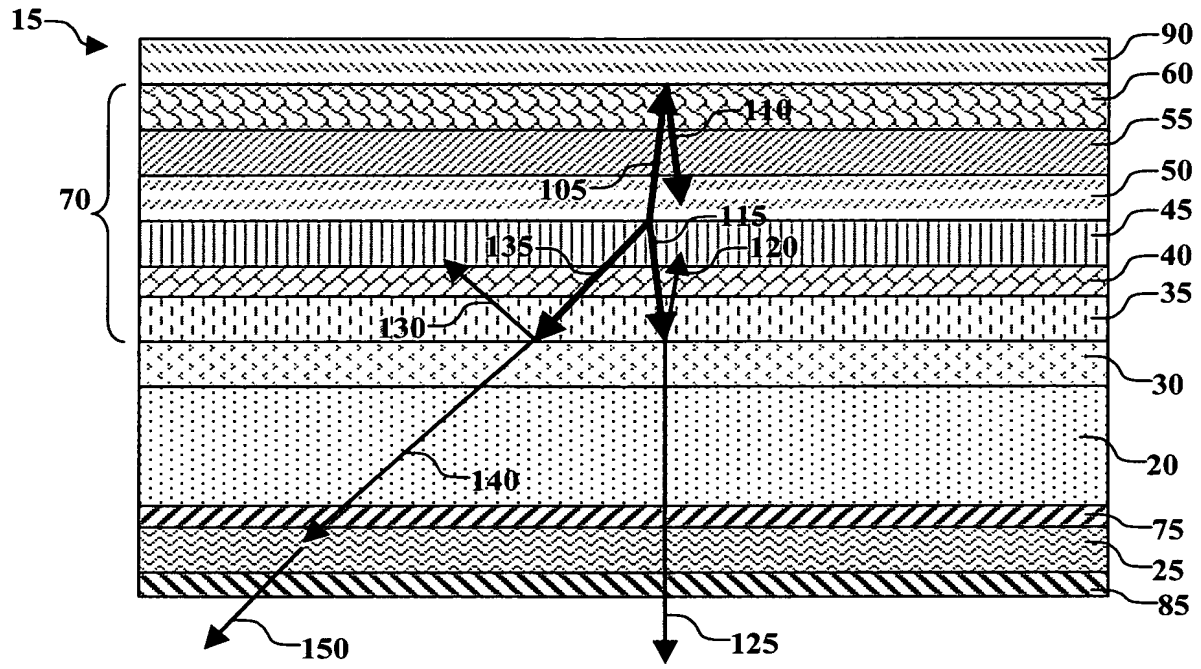


FIG. 3



A detailed cross-sectional view of a semiconductor device, labeled 65, showing three distinct regions: 80a, 80b, and 80c. Each region contains a vertical stack of layers. In region 80a, the layers are labeled 25a, 75a, 85a, and 125a. In region 80b, they are 25b, 75b, 85b, and 125b. In region 80c, the top layer is 125c. Below these, a common layer 100 is shown. Further down, there are layers 35a, 90a, 35b, 90b, and 90c. At the bottom, there is a substrate 20 with various components including 210, 211, 212, 213, 214, 215, 216, and 217. Large upward-pointing arrows are positioned above each of the three regions (80a, 80b, 80c).